

1/7

FIG.1

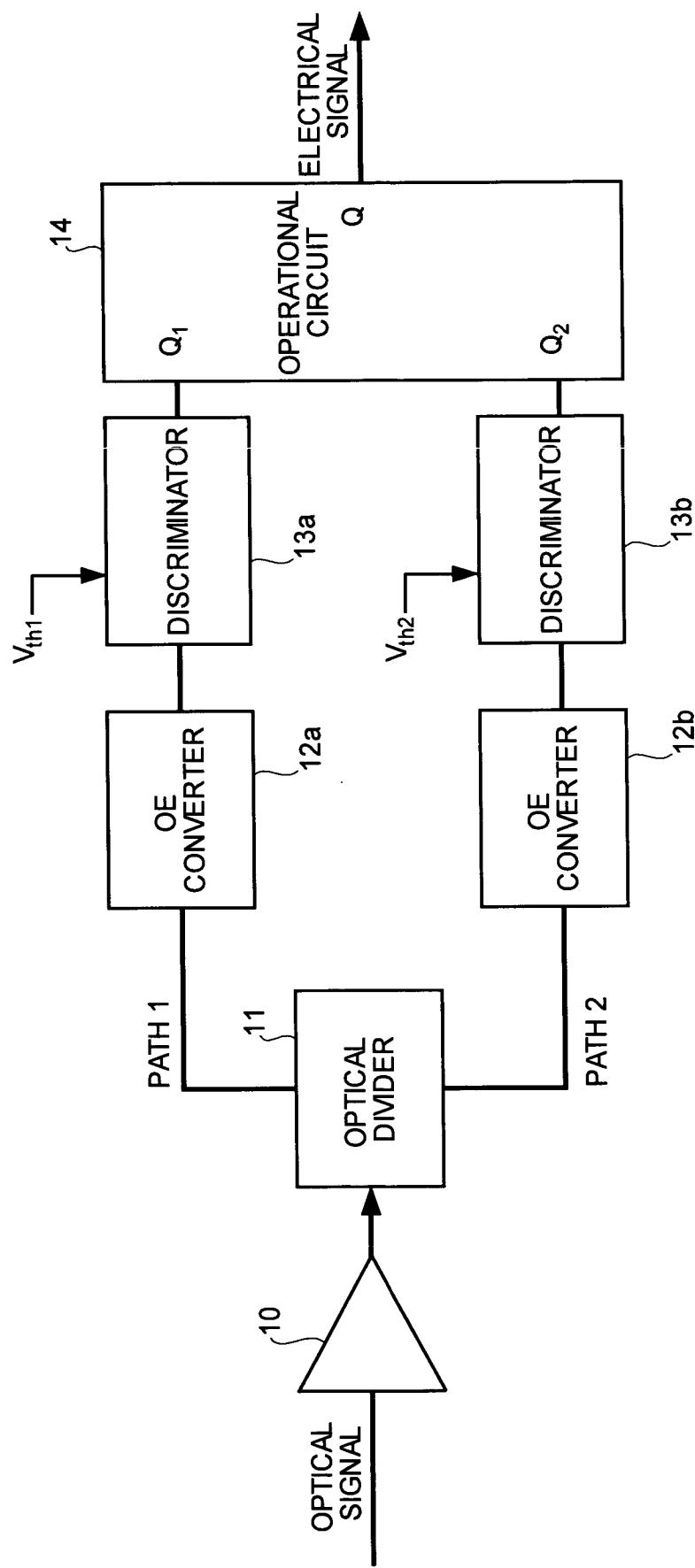


FIG.2A

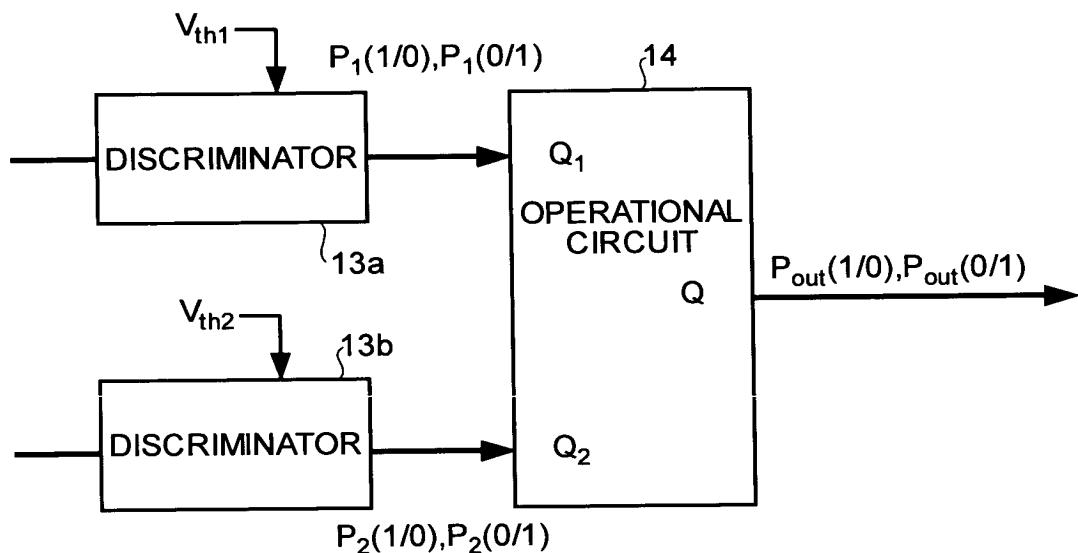


FIG.2B

TRUTH TABLE OF OR CIRCUIT

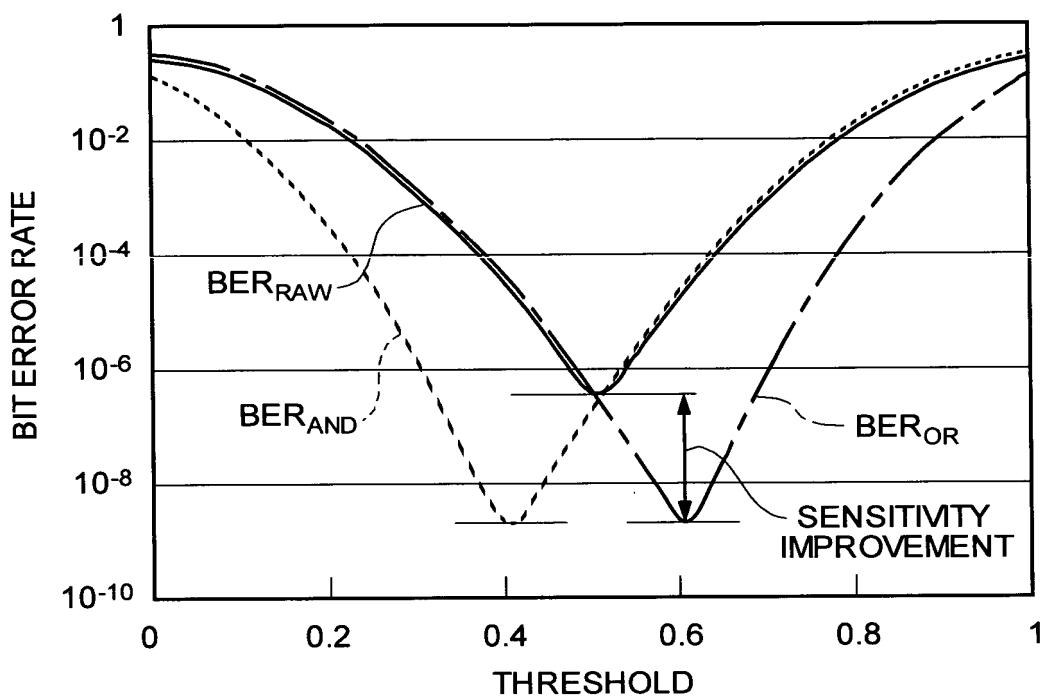
Q1	Q2	Q
0	0	0
1	0	1
0	1	1
1	1	1

FIG.2C

TRUTH TABLE OF AND CIRCUIT

Q1	Q2	Q
0	0	0
1	0	0
0	1	0
1	1	1

FIG.3



4/7

FIG. 4

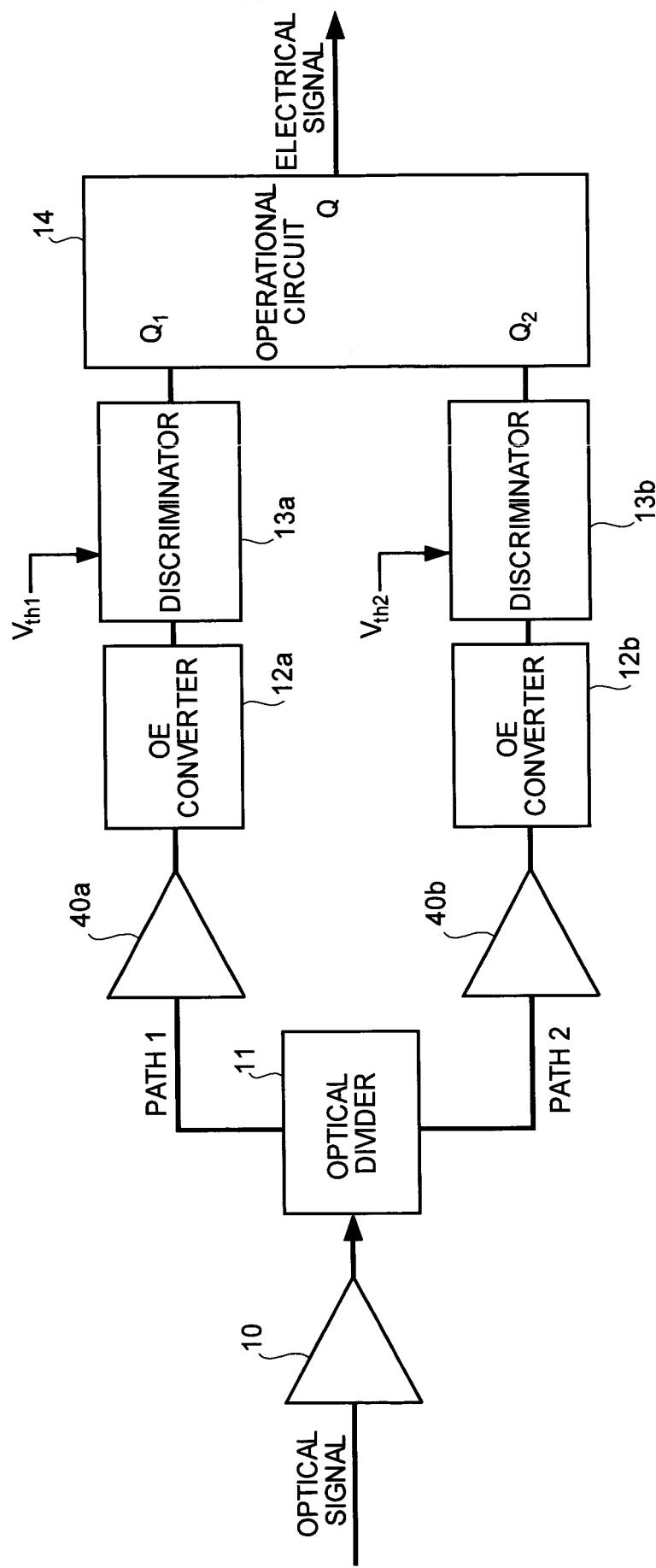
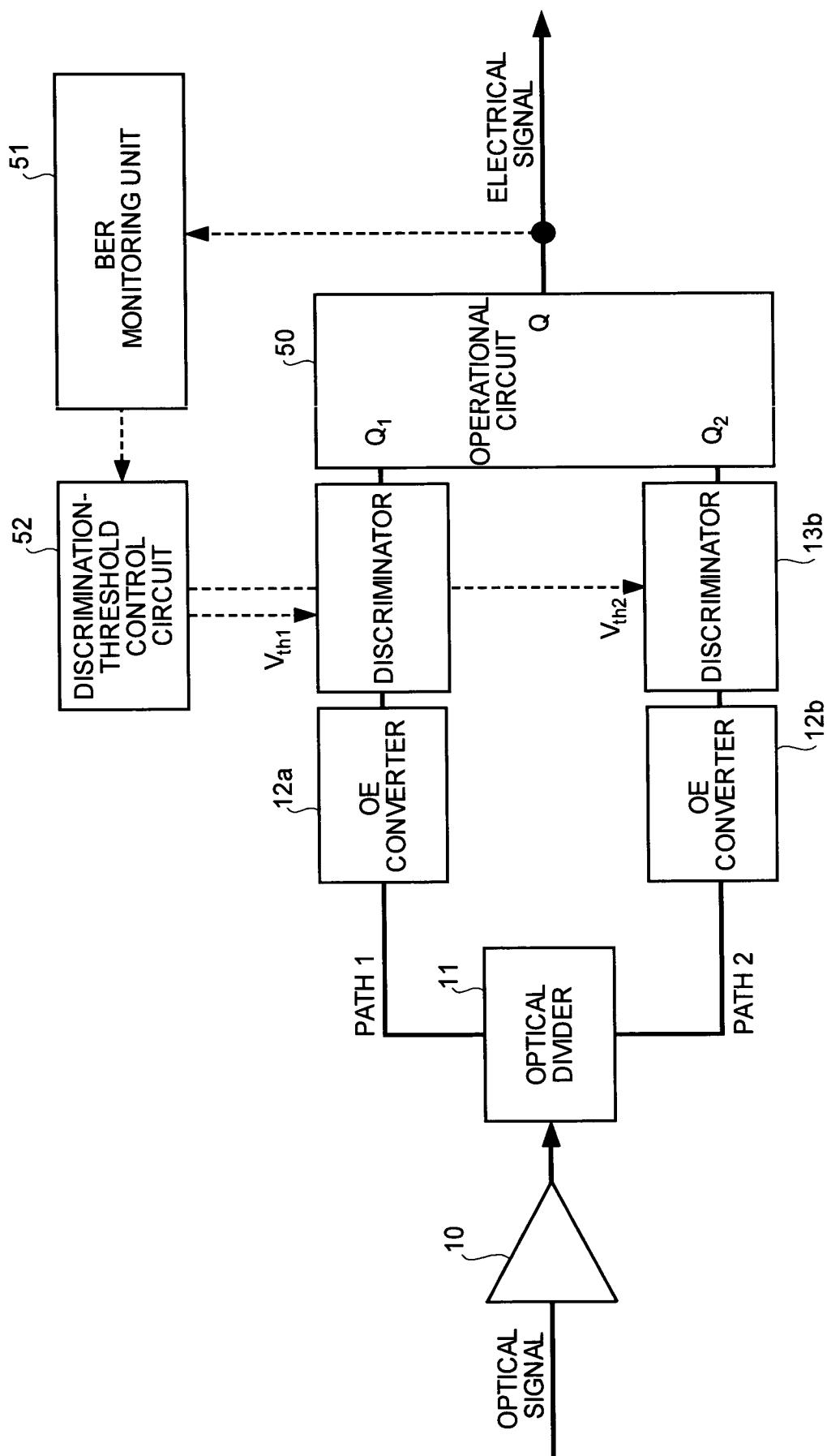
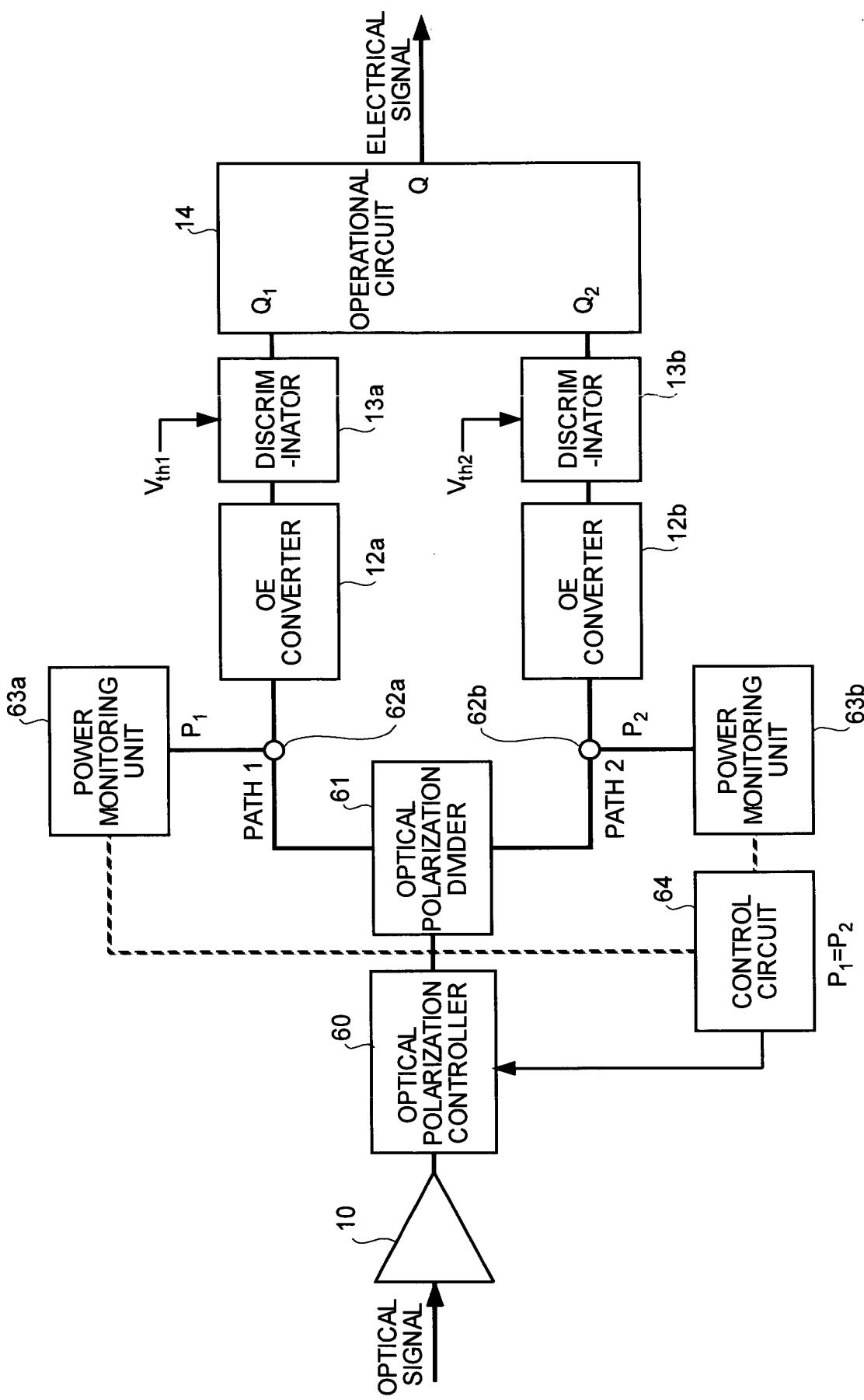


FIG.5



6/7

FIG. 6



7/7

FIG. 7

